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U.S. UTILITY Patent Application

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER Russell, K
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Masahiro Kobayashi Taiichi Miya

Resolver, resolver fault detection circuit, and resolver fault detection method $% \left(1\right) =\left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(1\right)$

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ISSUING CLASSIFICATION								
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TERMINAL		DRAWINGS		CLAIMS ALLOWED		
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not extend beyond the expiration date of U.S Patent. No.	, w			ISSUE FEE		
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